

**Amendments to the Written Description of the Specification**

Applicant presents replacement paragraphs below indicating the changes with insertions indicated by underlining and deletions indicated by strikeouts and/or double bracketing.

**Please replace the paragraph beginning at page 18, line 10 as follows:**

As illustrated in Fig. 4, after having calculated the present imaginary part  $X_1$  of the impedance of the terminal's oscillating circuit, its module is compared (block 45) to the ~~module~~ absolute value of limiting value  $X_{1\text{lim}}$ .

**Please replace the paragraph beginning at page 18, line 13 as follows:**

If the ~~current-module~~ present absolute value is greater than or equal to the limiting ~~module~~absolute value, it may be proceeded as indicated hereabove and the forcing value of relation 10 hereabove is applied (block 46).

**Please replace the paragraph beginning at page 18, line 16 as follows:**

If the ~~current-module~~ present absolute value is smaller than the limiting ~~module~~absolute value, it is attempted to determine on which side of the off-load value it is to be found. The ratios of the measured and off-load voltage  $VC_1$  and current  $I$  are thus measured (block 47). This amounts to determining whether imaginary part  $X_1$  is positive or negative.